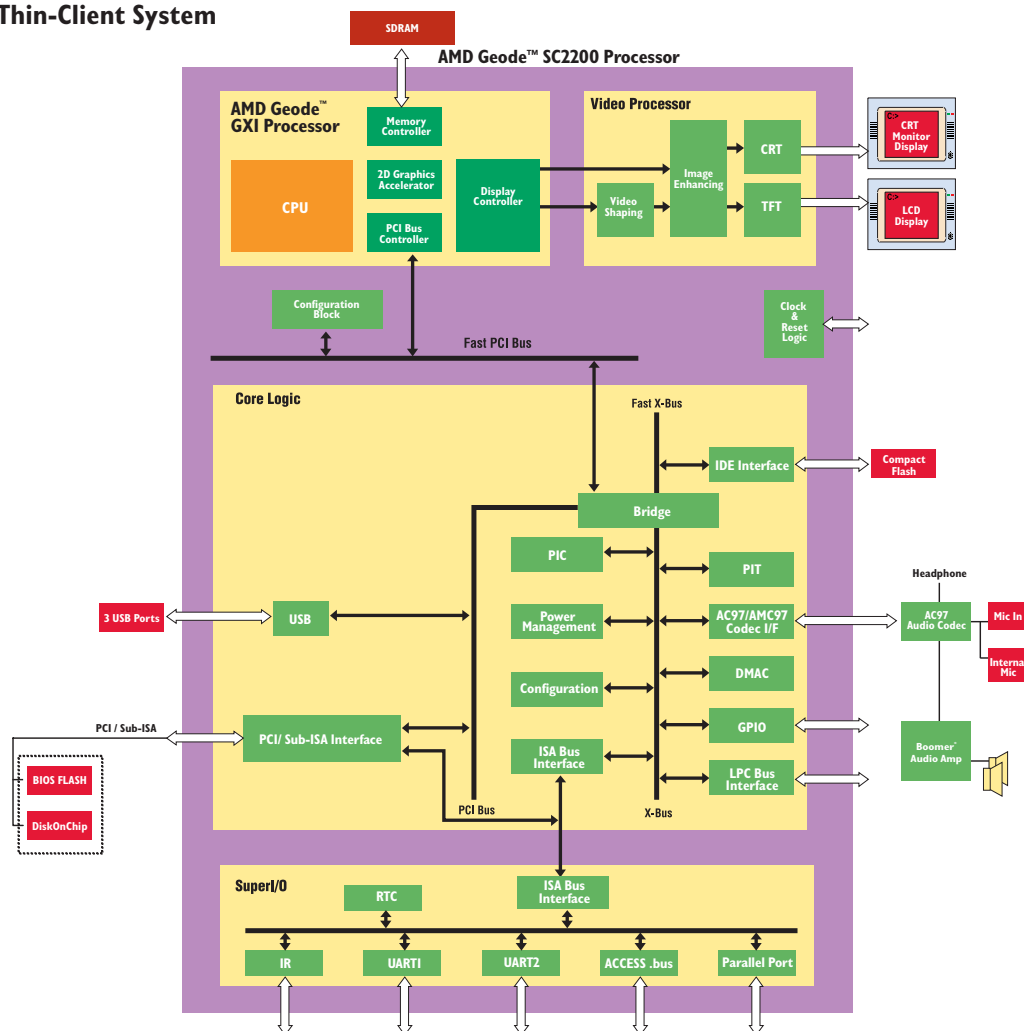


# Simplify system design for thin-client solutions



## Thin-Client System



The AMD Geode™ SC2200 processor is a member of the AMD Geode™ family of fully integrated x86 system chips. The SC2200 single-chip processor includes an AMD Geode™ GXI 32-bit x86-compatible processor, a CRT and TFT display processor, core logic, and a Super I/O block. These features, combined with the small form factor and low power consumption, make it ideal as the core of a thin client.

In particular, the SC2200 processor is an optimal solution for thin clients that are integrated into a Flat Panel Display (FPD).

The integrated architecture of the Geode SC2200 processor simplifies system design by reducing component count, the size of the main system board, and overall system power consumption. It can significantly lower overall system costs while improving time-to-market.

## Technical Specifications

### Primary Components

- The AMD Geode™ SC2200 processor is based on the AMD Geode™ GXI processor's integrated core which combines advanced CPU performance with MMX™ support, fully accelerated 2D graphics, a 64-bit synchronous DRAM (SDRAM) interface, and an internal PCI bus controller.
- Low power consumption CRT and TFT display processor, with a hardware video accelerator for scaling, filtering, and color space conversion.
- Core Logic module includes PC AT functionality, a Universal Serial Bus (USB) interface, ACPI I.0 compliant power management, and an audio codec interface.
- Super I/O block, including two serial ports, an infrared (IR) port, a parallel port, an ACCESS.bus interface, and a Real Time Clock (RTC).

The block diagram on the previous page shows the relationships between the functional blocks in the SC2200 processor.

### Outstanding Features

- 32-bit x86 processor, up to 266 MHz, with MMX instruction set support
- 64-bit SDRAM interface
- 2D graphics accelerator
- TFT interface: 1024 x 768 non-interlaced TFT @ 16 bpp graphics, up to 60 Hz
- PC AT functionality
- PCI bus controller
- Low Pin Count (LPC) Bus interface, specification rev. I.0 compatible
- Three Open HCI I.0 compliant USB ports
- AC97/AMC97 2.0 compliant audio controller
- Virtual System Architecture™ (VSA™) support
- Power management, ACPI I.0 compliant
- IDE channel for up to 2 external IDE devices; ATA-33 compliant
- Real Time Clock (RTC)
- 432-pin EBGA (Enhanced Ball Grid Array) package
  - 40 mm x 40 mm
- 481-pin TEPBGA (Thermally Enhanced Ball Grid Array) package
  - 40 mm x 40 mm

### About AMD

Founded in 1969 and based in Sunnyvale, California, AMD (NYSE: AMD) is a global supplier of integrated circuits for the personal and networked computer and communications markets with manufacturing facilities in the United

States, Europe, Japan, and Asia. AMD, a Standard & Poor's 500 company, produces microprocessors, Flash memory devices, and silicon-based solutions for communications and networking applications.

**AMD**   
**www.amd.com**

One AMD Place  
P.O. Box 3453,  
Sunnyvale, CA 94088-3453, USA  
Tel: 408-749-4000 or 800-538-8450  
TWX: 910-339-9280  
TELEX: 34-6306



### Technical Support

**USA & Canada:** 800-222-9323 or 408-749-5703  
**USA & Canada PC Microprocessor:**  
408-749-3060  
**USA & Canada Email:** hw.support@amd.com

**Latin America Email:**  
latinamerica.support@amd.com

**Europe & UK:** +44-0-1276-803299  
**Fax:** +44-0-1276-803298  
**France:** 0800-908-621  
**Germany:** +49-089-450-53199  
**Italy:** 800-877224  
**Europe Email:** euro.tech@amd.com

**Far East Fax:** 852-2956-0588

**Japan Fax:** 81-3-3346-7848

### Literature Ordering

**On the Web:** [www.amd.com/support/literature.html](http://www.amd.com/support/literature.html)  
**USA & Canada:** 800-222-9323  
**Europe Email:** euro.lit@amd.com  
**Far East Fax:** 852-2956-0588  
**Japan Fax:** 03-3346-9628

© 2003 Advanced Micro Devices, Inc. AMD, the AMD Arrow logo, and combinations thereof, and Geode and Virtual System Architecture are trademarks of Advanced Micro Devices, Inc. MMX is a trademark of Intel Corporation. Other product and company names used in this publication are for identification purposes only and may be trademarks of their respective companies.